Amendments to the Specification

Please amend the paragraph starting at page 11, line 1 as follows:

Fig. 15 shows a typical receiver of a modulated signal as modified according to the present invention. The modulated signal is received at an antenna **10** and shifted in frequency by a down converter **12**. The downconverted signal is then filtered by a bandpass filter **14**, the output of which is input to respective quadrature demodulators **16, 22** and a carrier recovery (CR) circuit **18**. The recovered carrier frequency is used to demodulate the filtered modulated signal in the demodulators **16, 22**, with the carrier frequency being shifted by a 90^0 phase shifter ~~**18**~~ **20** before being applied to one of the demodulators. The respective demodulated outputs are filtered by lowpass filters **24, 26**, and the filtered signals are digitized by analog-to-digital (A/D) converters **32, 34** before being stored in memory **36** as quadrature (I and Q) component values. The sample clock for the A/D converters **32, 34** is provided from a symbol timing recovery (STR) circuit **28** that generates from the filtered signals a symbol clock. So far the receiver described is conventional. See page 142 of "Wireless Digital Communications" by Kamilo Feher, Prentice-Hall, May 17, 1995.